

## **PATENT** (5500-88700/TT5374)

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:

10/726,902

Filed:

December 3, 2003

Inventor(s):

Mitchell Alsup Gregory W. Smaus

Title:

Transitioning From Instruction

Cache to Trace Cache on

Label Boundaries

Examiner:

Unknown

Group/Art Unit:

2183

Atty. Dkt. No:

5500-88700

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Robert C. Kowert Name of Registered Representative

## SUBMISSION OF FORMAL DRAWINGS

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicant hereby submits the formal drawings (Figures 1-6 on 6 sheets) for the above-referenced application and requests that these drawings be accepted for filing.

Respectfully submitted,

Robert C. Kowert

Reg. No. 39,255

ATTORNEY FOR APPLICANTS

Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C.

P.O. Box 398

Austin, TX 78767-0398

(512) 853-8800

Date: Avg. 11, 2004